



EMC TEST REPORT

For

Shenzhen Siyoteam Technology Co., Ltd

Product Name: DESKTOP CHARGER

Model No.: A8101

Prepared for : Shenzhen Siyoteam Technology Co., Ltd
Address : Room 2003B, SEG Plaza, huaqiang North Road, Futian District,
shenzhen, PR.China

Prepared by : SHENZHEN POCE TECHNOLOGY CO., LTD.
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Report No. : POCE17110612KRE
Date of Receiver : Nov. 06, 2017
Number of tested samples : 1
Serial number : Prototype
Date of Test : Nov. 06, 2017 – Nov. 13, 2017
Date of Report : Nov. 13, 2017

Note: This test report is limited to the above client company and the product model only. It may not be duplicated without prior written consent of Shenzhen POCE Technology Co., Ltd

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FIGURE.....	错误!未定义书签。
Photos of EUT	

1. GENERAL INFORMATION

1.1. Description of Device (EUT)

EUT	:	DESKTOP CHARGER
Trade Name	:	TEGUAR
Model	:	A8101
Supplementary Model	:	N/A
Test Voltage	:	AC 230V/50Hz
Rating	:	Input: AC 100240V, 50/60Hz Output: DC 3.6~ 6V, 3A Output: DC 6 ~ 9V, 2A Output: DC 9 ~ 12V, 1.5A AUTO Output: 5V, 10A TOTAL Power: 50W max
Applicant	:	Shenzhen Siyoteam Technology Co., Ltd
Address	:	Room 2003B, SEG Plaza, huaqiang North Road, Futian District, shenzhen, PR.China
Manufacturer	:	Denice Electronic Technology Co. Ltd.
Address	:	Jiangangling Industrial Park Yangchun City Guangdong China
Test Standards	:	EN 55032:2015 EN 55024:2010+A1:2015 EN61000-3-2:2014 EN61000-3-3:2013
Date of Receiver	:	Nov. 06, 2017
Date of Test	:	Nov. 06, 2017~Nov. 13, 2017
Test Engineer	:	
Reviewed By:	:	

1.2. Test Standards

- √ Indicates that the test is applicable
 × Indicates that the test is not applicable

Standard	Test Items	Status
EN 55032:2015	Disturbance Voltage at The Mains Terminals (150KHz To 30MHz)	√
	Radiated Disturbances (30MHz To 1000MHz)	√
EN61000-3-2:2014	Harmonic Current	×
EN61000-3-3:2013	Voltage Fluctuations	√
EN61000-4-2:2009	Electrostatic discharge Immunity	√
EN61000-4-3:2006	Radiated Susceptibility (80MHz to 1GHz)	√
EN61000-4-4:2012	Electrostatic Fast Transient/Burst Immunity	√
EN61000-4-5:2014	Surge Immunity	√
EN61000-4-6:2014	Conducted Susceptibility (150KHz to 80MHz)	√
EN61000-4-8:2010	Power Frequency Magnetic Field Immunity (50/60Hz)	√
EN61000-4-11:2004	Voltage Dips Short Interruptions Immunity Tests	√

1.3. Test Methodology

All measurements contained in this report were conducted with CISPR 16-1, radio disturbance and immunity measuring apparatus, and CISPR16-2, Method of measurement of disturbances and immunity.

All measurement required was performed at laboratory of Shenzhen POCE Technology Co., Ltd., at H Building, Hongfa Science and Technology Park, Tangtou, Shiyan, Bao'an District, Shenzhen, China

1.4. Test Facility

The test facility is recognized, certified, or accredited by the following organizations:

FCC – Registration No.: 222278

EMC Laboratory has been registered and fully described in a report filed with the (FCC) Federal Communications Commission. The acceptance letter from the FCC is maintained in our files. Registration 222278.

The facility also complies with the radiated and AC line conducted test site criteria set forth in CISPR 16-1, CISPR16-2.

2. MEASURING DEVICE AND TEST EQUIPMENT

2.1. For Power Line Conducted Emission

Item	Equipment	Manufacturer	Model No.	Factory Number	Last Cal.
1.	Test Receiver	Rohde & Schwarz	ESPI TEST RECEIVER	ID:1164.6607K03-102109-MH	Dec. 28, 2016
2.	L.I.S.N	Rohde & Schwarz	ESH3-Z5.831.5518.52	9561-G071	Dec. 28, 2016
3.	50ΩCoaxial Switch	Anritsu	MP59B	M20531	N/A
4.	Pulse Limiter	SCHWARZ BECK	VTSD 9561-F Pulse limiter 10dB Ateennator	561-G071	Dec. 28, 2016
5.	Cable	SCHWARZ BECK	N/A	N/A	Dec. 28, 2016

2.2. For Radiated Emission Measurement

Item	Equipment	Manufacturer	Model No.	Factory Number	Last Cal.
1.	Test Receiver	Rohde & Schwarz	ESPI TEST RECEIVER	ID:1164.6607K03-102109-MH	Dec. 28, 2016
2.	Bilog Antenna	Sunol Sciences	Model JB6 Antenna	A090414	Dec. 28, 2016
3.	50ΩCoaxial Switch	Anritsu	MP59B	M20531	N/A
4.	control	Positioning Controller	Model MF-7802	MF780208362	Dec. 28, 2016
5.	Cable	SCHWARZ BECK	N/A	N/A	Dec. 28, 2016
6.	Cable	SCHWARZ BECK	N/A	N/A	Dec. 28, 2016

2.3. For Harmonic Current / Flicker Measurement

Item	Equipment	Manufacturer	Model No.	Factory Number	Last Cal.
1.	Coupling decoupling network	SCHAFFNER	M016	20812	Dec. 28, 2016
2.	PC	N/A	P2L97	N/A	Dec. 28, 2016

2.4. For Electrostatic Discharge Immunity Test

Item	Equipment	Manufacturer	Model No.	Factory Number	Last Cal.
1.	ESD Tester	Prima	ESD61002A	144305	Dec. 28, 2016

2.5. For RF Strength Susceptibility Test

Item	Equipment	Manufacturer	Model No.	Factory Number	Last Cal.
1.	Signal	HP	8648A	3625U00573	Dec. 28, 2016

	Generator				
2.	Amplifier	AR	500A100	17034	NCR
3.	Amplifier	AR	100W/1000M1	17028	NCR
4.	Isotropic Field Monitor	AR	FM2000	16829	NCR
5.	Isotropic Field Probe	AR	FP2000	16755	Dec. 28, 2016
6.	Biconic Antenna	EMCO	3108	9507-2534	NCR
7.	Log-periodic Antenna	AR	AT1080	16812	NCR
8.	PC	N/A	486DX2	N/A	N/A

2.6. For Electrical Fast Transient /Burst Immunity Test

Item	Equipment	Manufacturer	Model No.	Factory Number	Last Cal.
1.	Burst Tester	HTEC	HEFT 51	144303	Dec. 28, 2016
2.	Coupling Clamp	HTEC	IP-4A	147147	Dec. 28, 2016

2.7. For Surge Immunity Test

Item	Equipment	Manufacturer	Model No.	Factory Number	Last Cal.
1.	Surge Tester	HTEC	HCWG	144302	Dec. 28, 2016

2.8. For Injected Current Susceptibility Test

Item	Equipment	Manufacturer	Model No.	Factory Number	Last Cal.
1.	Simulator	EMTEST	CWS500C	0900-12	Dec. 28, 2016
2.	CDN	EMTEST	CDN-M2	5100100100	Dec. 28, 2016
3.	CDN	EMTEST	CDN-M3	0900-11	Dec. 28, 2016
4.	Injection Clamp	EMTEST	F-2031-23MM	368	Dec. 28, 2016
5.	Attenuator	EMTEST	ATT6	0010222A	Dec. 28, 2016

2.9. For Magnetic Field Immunity Test

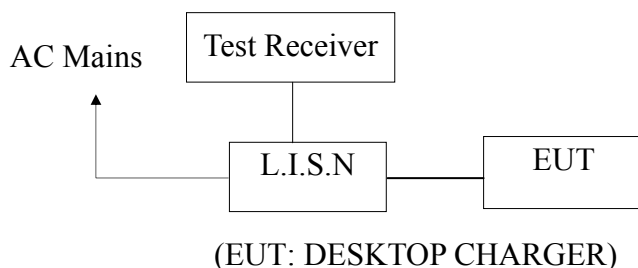
Item	Equipment	Manufacturer	Model No.	Factory Number	Last Cal.
1.	Magnetic Field Tester	HTEC	HPFM T	144301	Dec. 28, 2016

2.10. For Voltage Dips and Interruptions Test

Item	Equipment	Manufacturer	Model No.	Factory Number	Last Cal.
1.	Dips Tester	HTEC	HPFS	144304	Dec. 28, 2016

3. POWER LINE CONDUCTED EMISSION MEASUREMENT

3.1. Block Diagram of Test Setup



3.2. Measuring Standard

EN 55032:2015

Power Line Conducted Emission Limits (Class B)

Frequency (MHz)	Limit (dB μ V)	
	Quasi-Peak Level	Average Level
0.15 ~ 0.50	66.0 ~ 56.0 *	56.0 ~ 46.0 *
0.50 ~ 5.00	56.0	46.0
5.00 ~ 30.00	60.0	50.0

NOTE1-The lower limit shall apply at the transition frequencies.
NOTE2-The limit decreases linearly with the logarithm of the frequency in the range 0.15MHz to 0.50MHz.

3.3. EUT Configuration on Measurement

The following equipments are installed on Conducted Emission Measurement to meet EN 55032 requirements and operating in a manner which tends to maximize its emission characteristics in a normal application.

3.4. Test Procedure

The EUT is put on the plane 0.8m high above the ground by insulating support and connected to the AC mains through Line Impedance Stability Network (L.I.S.N). This provided a 50ohm coupling impedance for the tested equipments. Both sides of AC line are investigated to find out the maximum conducted emission according to the EN 55032 regulations during conducted emission measurement.

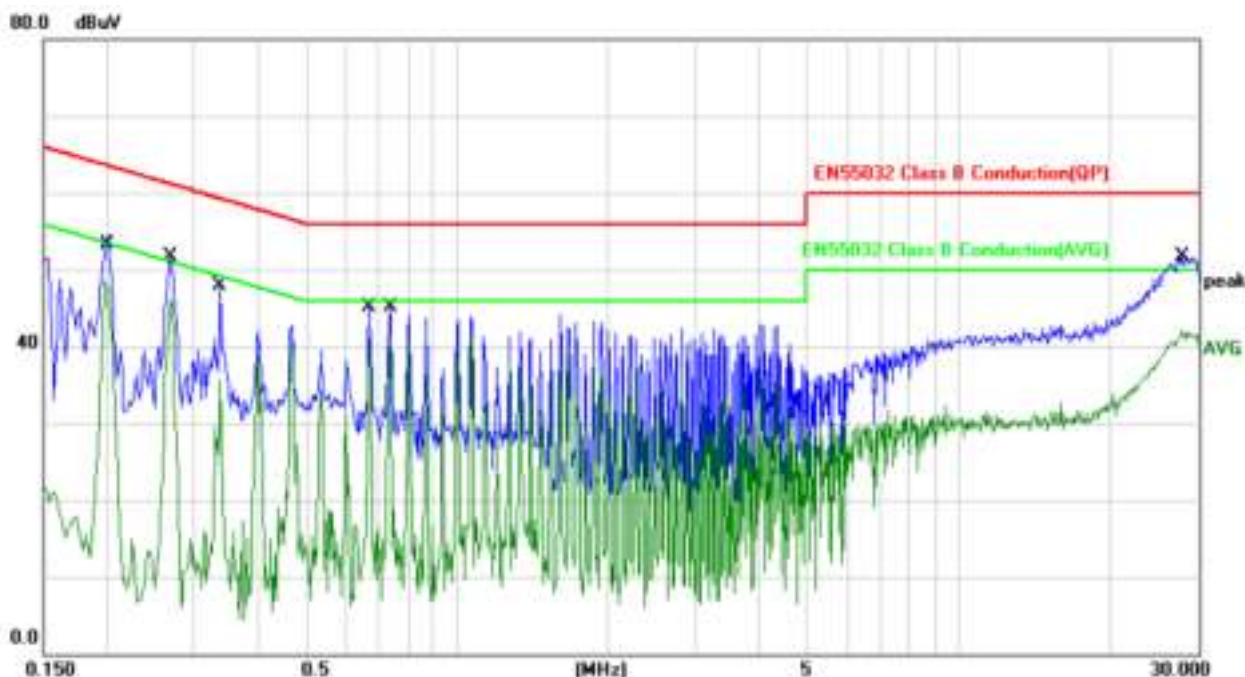
The bandwidth of the field strength meter (R&S Test Receiver ESCS30) is set at 9KHz in 150KHz~30MHz and 200Hz in 9KHz~150KHz.

The frequency range from 150kHz to 30MHz is investigated.

Conduction Uncertainty: $U_c = \pm 2.72$ dB

Conducted Emission Test Data

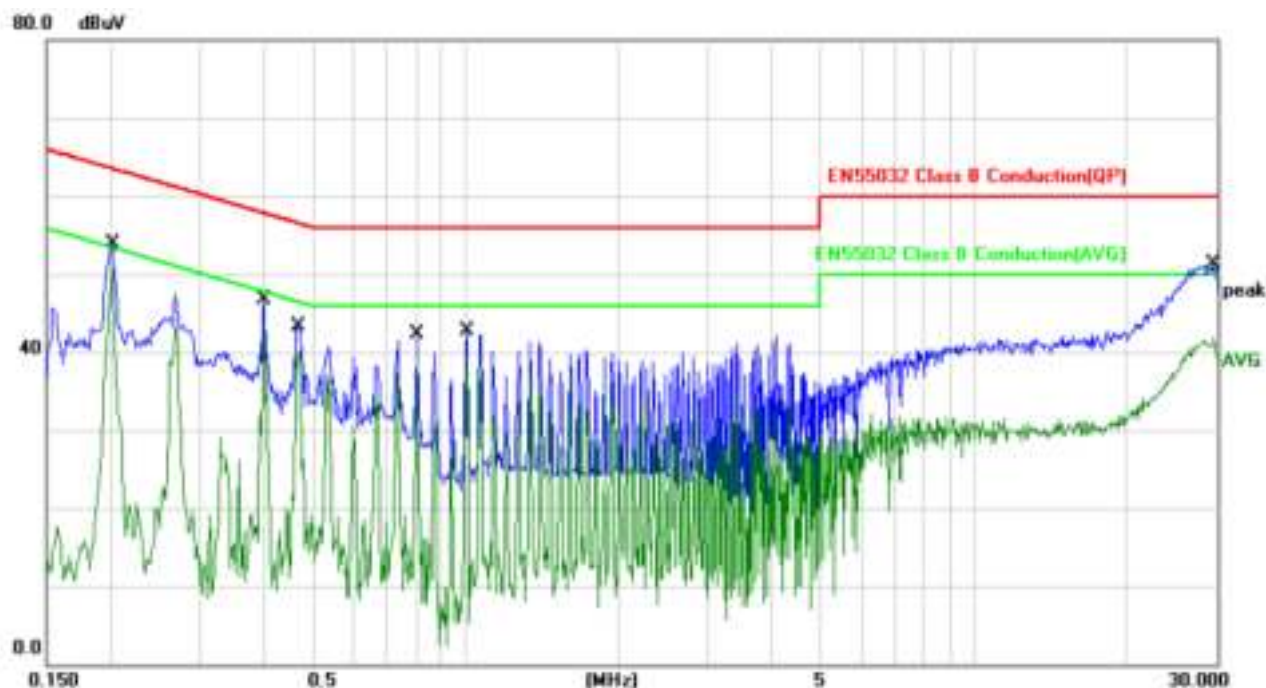
EUT	: DESKTOP CHARGER	Temperature:	20°C
M/N	: A8101	Humidity	: 50%
Test Voltage	: AC 230V/50Hz	Test Mode	: Full load
Test Engineer	: Bill	Phase	: L-Line



No.	Mk.	Freq.	Reading Level	Correct Factor	Measurement	Limit	Over	Detector	Comment
		MHz	dBuV	dB	dBuV	dBuV	dB		
1		0.1980	54.30	0.19	54.49	63.69	-9.20	QP	
2		0.1980	48.26	0.19	48.45	53.69	-8.24	AVG	
3		0.2700	51.45	0.25	51.70	61.12	-9.42	QP	
4		0.2700	45.76	0.25	46.01	51.12	-8.11	AVG	
5		0.3379	47.57	0.32	47.89	59.25	-11.36	QP	
6		0.3379	35.28	0.32	35.60	49.25	-13.65	AVG	
7		0.6700	44.87	0.33	45.20	55.00	-10.80	QP	
8		0.6700	40.81	0.33	41.14	45.00	-4.86	AVG	
9		0.7340	39.54	0.33	39.87	55.00	-16.13	QP	
10	*	0.7340	44.86	0.33	45.19	45.00	-0.81	AVG	
11		27.9420	51.87	-0.11	51.76	60.00	-8.24	QP	
12		27.9420	42.18	-0.11	42.07	50.00	-7.93	AVG	

Conducted Emission Test Data

EUT	: DESKTOP CHARGER	Temperature:	20°C
M/N	: A8101	Humidity	: 50%
Test Voltage	: AC 230V/50Hz	Test Mode	: Full load
Test Engineer	: Bill	Phase	: N-Line

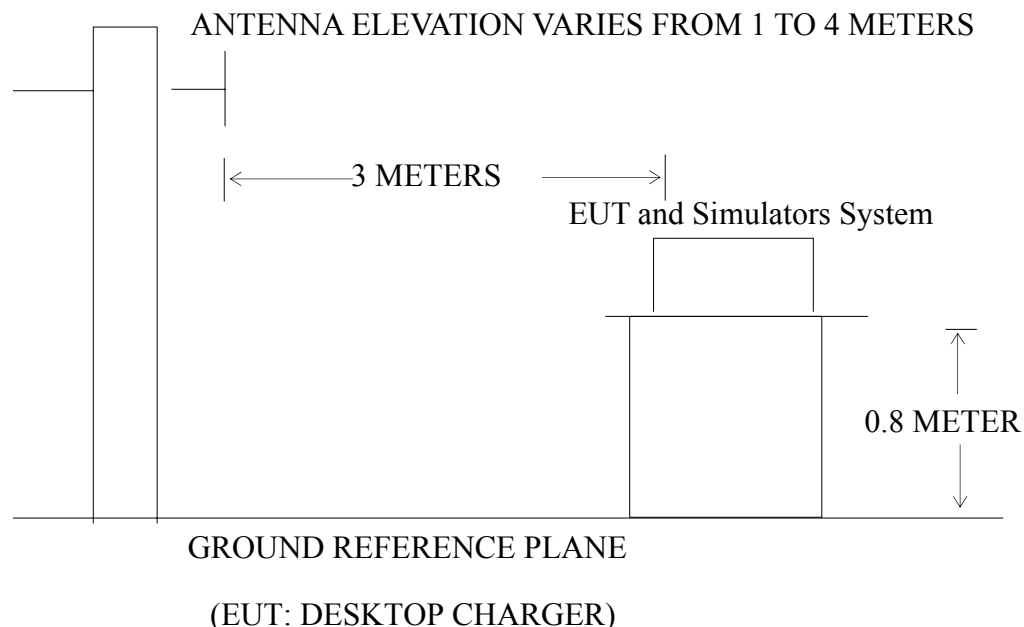


No.	Mk.	Freq. MHz	Reading Level dBuV	Correct Factor dB	Measurement dBuV	Limit dBuV	Over dB	Detector	Comment
1		0.2020	53.66	0.19	53.85	63.52	-9.67	QP	
2	^	0.2020	50.53	0.19	50.72	53.52	-2.80	AVG	
3		0.4020	46.31	0.32	46.63	57.81	-11.18	QP	
4		0.4020	43.34	0.32	43.66	47.81	-4.15	AVG	
5		0.4700	42.92	0.32	43.24	56.51	-13.27	QP	
6		0.4700	39.99	0.32	40.31	46.51	-6.20	AVG	
7		0.8020	42.04	0.32	42.36	56.00	-13.64	QP	
8		0.8020	38.16	0.32	38.48	46.00	-7.52	AVG	
9		1.0060	42.46	0.29	42.75	56.00	-13.25	QP	
10		1.0060	39.14	0.29	39.43	46.00	-6.57	AVG	
11		29.4740	51.35	-0.12	51.23	60.00	-8.77	QP	
12		29.4740	41.98	-0.12	41.86	50.00	-8.14	AVG	

4. RADIATED EMISSION MEASUREMENT

4.1. Block Diagram of Test

4.1.1. Block diagram of test setup (In chamber)



4.2. Measuring Standard

EN 55032:2015

Radiated Emission Limits

All emanations from a class B device or system, including any network of conductors and apparatus connected thereto, shall not exceed the level of field strengths specified below:

FREQUENCY (MHz)	DISTANCE (Meters)	FIELD STRENGTHS LIMIT (dB μ V/m)
30 ~ 230	3	40
230 ~ 1000	3	47

Note: (1) The smaller limit shall apply at the combination point between two frequency bands.

(2) Distance refers to the distance in meters between the measuring instrument antenna and the closed point of any part of the EUT.

4.3. EUT Configuration on Test

The EN 55032 regulations test method must be used to find the maximum emission during radiated emission measurement.

4.4. Test Procedure

The EUT is placed on a turn table which is 0.8 meter high above the ground. The turn table can rotate 360 degrees to determine the position of the maximum emission level. The EUT is set 3 meters away from the receiving antenna which is mounted on a antenna tower. The antenna can be moved up and down from 1 to 4 meters to find out the maximum emission level. Bilog antenna (calibrated by Dipole Antenna) is used as a receiving antenna. Both horizontal and vertical polarization of the antenna are set on test.

The bandwidth of the Receiver (ESCS30) is set at 120kHz.
The frequency range from 30MHz to 1000MHz is investigated.

Radiation Uncertainty: $U_r = \pm 3.84$ dB

Radiated Emission Test Data

EUT	: DESKTOP CHARGER	Temperature:	20°C
M/N	: A8101	Humidity	: 50%
Test Voltage	: AC 230V/50Hz	Test Mode	: Full load
Test Engineer	: Bill	Polarization	: Horizontal



No.	Mk.	Freq. MHz	Reading Level dBuV	Correct Factor dBm	Measure- ment dBuV/m	Limit dBuV/m	Over dB	Detector	Antenna Height cm	Table Degree degree	Comment
1		124.1330	41.70	-13.96	27.74	40.00	-12.26	QP			
2		197.8928	48.15	-14.65	33.50	40.00	-6.50	QP			
3		210.0482	48.85	-16.10	32.75	40.00	-7.25	QP			
4		225.3080	48.71	-15.57	33.14	40.00	-6.86	QP			
5		265.6757	49.43	-13.35	36.08	47.00	-10.92	QP			
6	^	111.7379	51.63	-14.87	36.76	40.00	-3.24	QP			

Radiated Emission Test Data

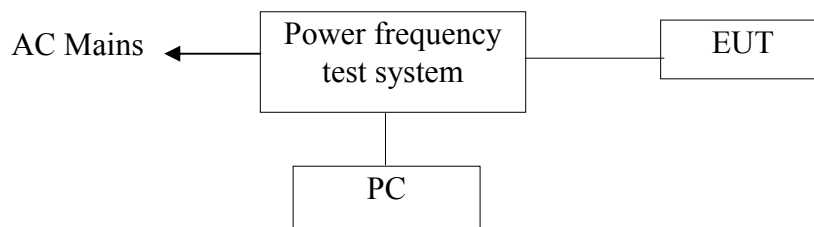
EUT	: DESKTOP CHARGER	Temperature:	20°C
M/N	: A8101	Humidity	: 50%
Test Voltage	: AC 230V/50Hz	Test Mode	: Full load
Test Engineer	: Bill	Polarization	: Vertical



No.	Mk.	Freq. MHz	Reading Level dBuV	Correct Factor dB(m)	Measure- ment dBuV(m)	Limit dBuV(m)	Over dB	Detector	Antenna Height cm	Table Degree degree	Comment
1	^	114.5146	51.00	-14.47	36.53	40.00	-3.47	QP			
2	!	128.1130	49.85	-13.81	36.04	40.00	-3.96	QP			
3	!	189.7385	52.34	-15.94	36.40	40.00	-3.60	QP			
4	!	32.2924	45.19	-8.67	36.52	40.00	-3.48	QP			
5	!	141.3298	50.70	-14.51	36.19	40.00	-3.81	QP			
6	!	98.1419	53.35	-16.95	36.40	40.00	-3.60	QP			

5. HARMONIC CURRENT EMISSION MEASUREMENT

5.1 Block Diagram of Test Setup



(EUT: DESKTOP CHARGER)

5.2 Measuring Standard

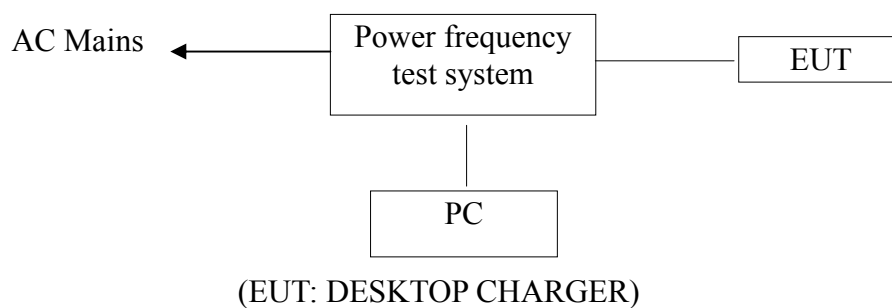
EN 61000-3-2: 2014

5.3 Description of test Equipment

Note: Equipment is less than 75W, no corresponding harmonic current limit.

6. VOLTAGE FLUCTUATION AND FLICKER MEASUREMENT

6.1 Block Diagram of Test Setup



6.2 Measuring Standard

EN61000-3-3:2013

Voltage Fluctuation Flicker Test Data

EN 61000-3-3 TEST REPORT 2017-11-08 11:25

Unit: DESKTOP CHARGER M/N: A8101
 Test mode: Full load

Manufacturer: N/A
 Operator: Bill

=====

TEST SETUP

Test Freq. : 50.00 Hz. Test Voltage: 231.0 vac
 Waveform : SINE
 Test Time: 120.0 min. Tshort: 10.0 min.
 Prog. Zo Enabled: YES Prog. Zo: 0.000

Voltage Change less than once per Hour: NO
 Impedance selected: IEC-725 STD. REF.

Synthetic R+L Enabled: NO
 Resistance: 0.380 Ohms Inductance: 460.000 uH

TEST DATA

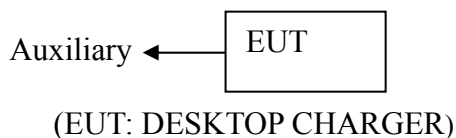
Result:	PASS			
	EUT Data	Limit	Result	Test Enabled
Pst max	0.180	1.00	PASS	true
Plt max	0.171	0.65	PASS	true
dc %	0.00	3.00	PASS	true
dmax %	1.52	4.00	PASS	true
d(t) sec.	0.00	0.20	PASS	true

	Power Source Data			
Source Pst max	0.234	0.400	PASS	true
% THD	0.03	3.00	PASS	true

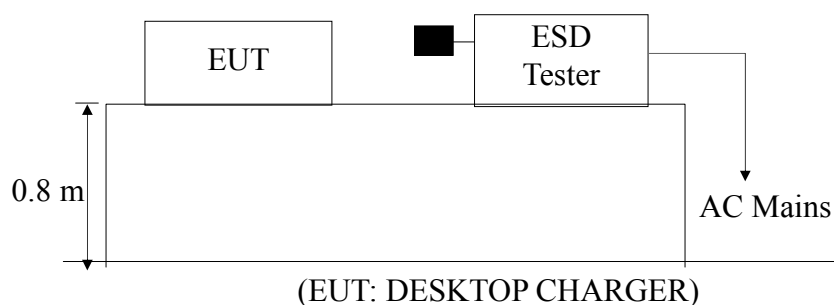
7. ELECTROSTATIC DISCHARGE IMMUNITY TEST

7.1 Block Diagram of Test Setup

7.1.1 Block Diagram of the EUT and the simulators



7.1.2 Block diagram of ESD test setup



7.2 Test Standard

EN 55024:2010+A1:2015 (EN61000-4-2: 2009)

Severity Level: 3 / Air Discharge: $\pm 8\text{KV}$ Level: 2 / Contact Discharge: $\pm 4\text{KV}$

7.3 Severity Levels and Performance Criterion

7.3.1 Severity level

Level	Test Voltage Contact Discharge (KV)	Test Voltage Air Discharge (KV)
1.	± 2	± 2
2.	± 4	± 4
3.	± 6	± 8
4.	± 8	± 15
X	Special	Special

7.3.2 Performance criterion: **B**

7.4 Test Procedure

7.4.1 Air Discharge:

This test is done on a non-conductive surface. The round discharge tip of the discharge electrode shall be approached as fast as possible to touch the EUT. After each discharge, the discharge electrode shall be removed from the EUT. The generator is then re-triggered for a new single discharge and repeated 10 times for each pre-selected test point. This procedure shall be repeated until all the air discharge completed.

7.4.2 Contact Discharge:

All the procedure shall be same as Section 7.4.1. except that the tip of the discharge electrode shall touch the EUT before the discharge switch is operated.

7.4.3 Indirect discharge for horizontal coupling plane

At least 10 single discharges (in the most sensitive polarity) shall be applied at the front edge of each HCP opposite the center point of each unit (if applicable) of the EUT and 0.1m from the front of the EUT. The long axis of the discharge electrode shall be in the plane of the HCP and perpendicular to its front edge during the discharge.

7.4.4 Indirect discharge for vertical coupling plane

At least 10 single discharge (in the most sensitive polarity) shall be applied to the center of one vertical edge of the coupling plane. The coupling plane, of dimensions 0.5m X 0.5m, is placed parallel to, and positioned at a distance of 0.1m from the EUT. Discharges shall be applied to the coupling plane, with this plane in sufficient different positions that the four faces of the EUT are completely illuminated.

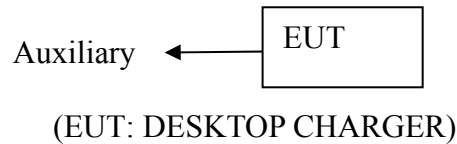
Electrostatic Discharge Test Result

EUT	: DESKTOP CHARGER	Temperature:	20℃
M/N	: A8101	Humidity	: 50%
Test Voltage	: AC 230V/50Hz	Test Mode	: Full load
Test Engineer	: Bill	Test Date	: 2017-11-08
Air Discharge: \pm 8KV # For each point positive 10 times and negative 10 times discharge. Contact Discharge: \pm 4KV # For each point positive 25 times and negative 25 times discharge.			
Location	Discharge Points	Kind A-Air Discharge C-Contact Discharge	Result
HCP	4 Points	C	PASS
VCP	4 Points	C	PASS
USB Port	8 Point	C	PASS
Slot	8 Points	A	PASS
Note: (The Criterion) A: Normal performance within the specification limits; B: Temporary degradation or less of function or performance which is self-recoverable; C: Temporary degradation or loss of function or performance which requires operator intervention or system reset;		Test Equipment :ESD Tester Model: ESD61002A	

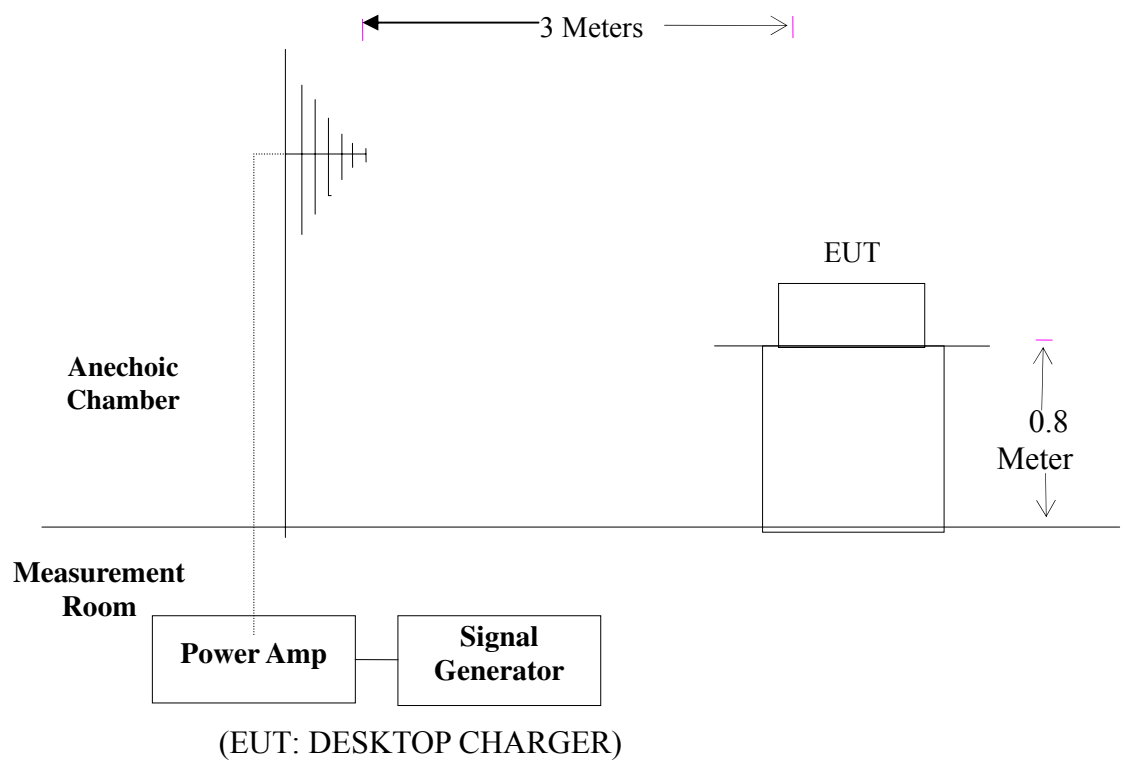
8. RF FIELD STRENGTH SUSCEPTIBILITY TEST

8.1 Block Diagram of Test

8.1.1 Block diagram of connection between the EUT and Load



8.1.2 Block diagram of RS test setup



8.2 Test Standard

EN 55024:2010+A1:2015(EN61000-4-3: 2006 (Severity Level: 2, 3V / m))

8.3 Severity Levels and Performance Criterion

8.3.1 Severity Levels

Level	Field Strength V/m
1.	1
2.	3
3.	10
X	Special

8.3.2 Performance Criterion: A

8.4 Test Procedure

The EUT are placed on a table which is 0.8 meter high above the ground. The EUT is set 3 meters away from the transmitting antenna which is mounted on an antenna tower. Both horizontal and vertical polarization of the antenna are set on test. Each of the four sides of the EUT must be faced this transmitting antenna and measured individually. In order to judge the EUT performance, a CCD camera is used to monitor its screen. All the scanning conditions are as following:

Condition of Test	Remark
1. Fielded Strength	3V/m (Severity Level 2)
2. Radiated Signal	Modulated
3. Scanning Frequency	80-1000MHz
4. Sweep time of radiated	0.0015 Decade/s
2. Dwell Time	1 Sec.

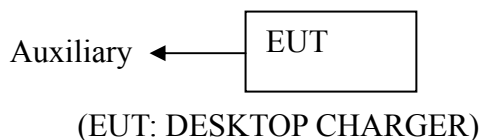
RF Field Strength Susceptibility Test Results

EUT	: DESKTOP CHARGER	Temperature	: 22℃			
M/N	: A8101	Humidity	: 50 %			
Test Voltage	: AC 230V/50Hz	Test Mode	: Full load			
Field Strength	: 3 V/m	Test Date	: 2017-11-08			
Test Engineer:	Bill	Frequency Range:	80 MHz to1000 MHz			
Modulation:		<input type="checkbox"/> None	<input type="checkbox"/> Pulse <input checked="" type="checkbox"/> AM 1KHz 80%			
Frequency Rang 1:		Frequency Rang 2:				
80~ 1000MHz						
Steps	1	/	%	#	/	%
	Horizontal	Vertical		Horizontal	Vertical	
Front	PASS	PASS				
Right	PASS	PASS				
Rear	PASS	PASS				
Left	PASS	PASS				
Test Equipment : 1. Signal Generator : 2031 (MARCONI) 2. Power Amplifier : 500A100 & 100W/1000M1 (A&R) 3. Power Antenna : 3108 (EMCO) & AT1080 (A&R) 4. Field Monitor : FM2000 (A&R)						
Note: Note: (The Criterion) A:Normal performance within the specification limits; B:Temporary degradation or less of function or performance which is self-recoverable; C:Temporary degradation or loss of function or performance which requires operator intervention or system reset;						

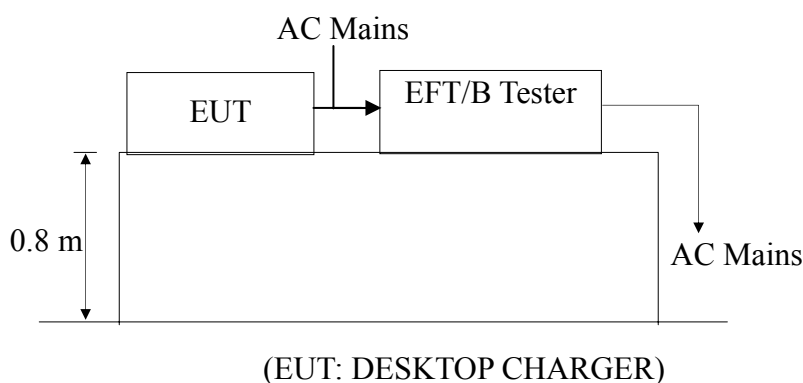
9. ELECTRICAL FAST TRANSIENT/BURST IMMUNITY TEST

9.1 Block Diagram of Test Setup

9.1.1. Block Diagram of the EUT



9.1.2. EFT Test Setup



9.2 Test Standard

EN 55024:2010+A1:2015 (EN61000-4-4:2012, Severity Level, Level 2: 1KV)

9.3 Severity Levels and Performance Criterion

9.3.1 Severity level

Open Circuit Output Test Voltage $\pm 10\%$		
Level	On Power Supply Lines	On I/O (Input/Output) Signal data and control lines
1.	0.5 KV	0.25 KV
2.	1 KV	0.5 KV
3.	2 KV	1 KV
4.	4 KV	2 KV
X	Special	Special

9.3.2 Performance criterion : **B**

9.4 Test Procedure

The EUT is put on the table which is 0.8 meter high above the ground. This reference ground

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plane shall project beyond the EUT by at least 0.1m on all sides and the minimum distance between EUT and all other conductive structure, except the ground plane beneath the EUT, shall be more than 0.5m.

9.4.1 For input and output AC power ports:

The EUT is connected to the power mains by using a coupling device which couples the EFT interference signal to AC power lines. Both polarities of the test voltage should be applied during compliance test and the duration of the test is 2 mins.

9.4.2 For signal lines and control lines ports:

No I/O ports. It's unnecessary to test.

9.4.3 For DC output line ports:

It's unnecessary to test.

Electrical Fast Transient/Burst Test Results

EUT	: DESKTOP CHARGER	Temperature:	20°C
M/N	: A8101	Humidity :	50%
Test Voltage	: AC 230V/50Hz	Test Mode :	Full load
Test Engineer	: Bill	Test Date :	2017-11-08
Line: <input checked="" type="checkbox"/> AC Mains <input type="checkbox"/> Signal line <input type="checkbox"/> DC line		Coupling : <input checked="" type="checkbox"/> Direct <input type="checkbox"/> Capacitive	

Test Time : 120s

Line	Test Voltage	Result
L	± 1KV	Pass
N	± 1KV	Pass
L、 N	± 1KV	Pass

Test Equipment: Burst Tester Model : HEFT 51

Note: (The Criterion)

A: Normal performance within the specification limits;

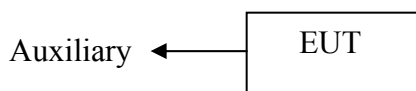
B: Temporary degradation or loss of function or performance which is self-recoverable;

C: Temporary degradation or loss of function or performance which requires operator intervention or system reset;

10. SURGE IMMUNITY TEST

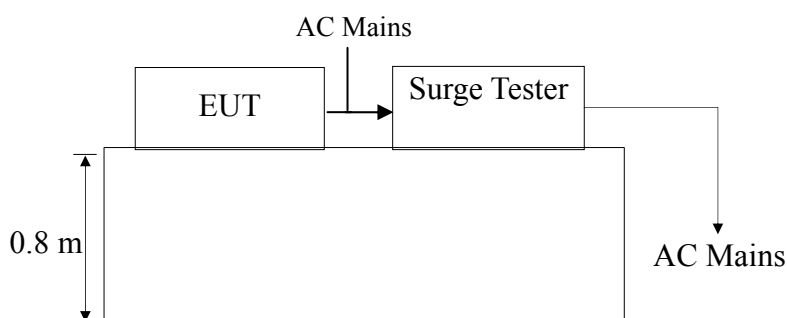
10.1 Block Diagram of Test Setup

10.1.1 Block Diagram of the EUT



(EUT: DESKTOP CHARGER)

10.1.2. Surge Test Setup



(EUT: DESKTOP CHARGER)

10.2 Test Standard

EN 55024:2010+A1:2015 (EN61000-4-5: 2014) Severity Level: Line to Line: Level 2, 1.0KV

10.3 Severity Levels and Performance Criterion

10.3.1. Severity level

Severity Level	Open-Circuit Test Voltage KV
1	0.5
2	1.0
3	2.0
4	4.0
*	Special

10.3.2 Performance criterion: **B**

10.3 Test Procedure

- 1) Set up the EUT and test generator as shown on Section 10.1.2.
- 2) For line to line coupling mode, provide a 1.0 KV 1.2/50us voltage surge (at open-circuit condition) and 8/20us current surge to EUT selected points.
- 3) At least 5 positive and 5 negative (polarity) tests with a maximum 1/min repetition rate are conducted during test.
- 4) Different phase angles are done individually.
- 5) Record the EUT operating situation during compliance test and decide the EUT immunity criterion for above each test.

Surge Immunity Test Result

EUT	: DESKTOP CHARGER	Temperature:	20℃		
M/N	: A8101	Humidity	: 50%		
Test Voltage	: AC 230V/50Hz	Test Mode	: Full load		
Test Engineer	: Bill	Test Date	: 2017-11-08		
Location	Voltage (KV)	1kV		2kV	
	Phase	+	-	+	-
L、N	0°	Pass	Pass		
	90°	Pass	Pass		
	180°	Pass	Pass		
	270°	Pass	Pass		
L、PE	0°				
	90°				
	180°				
	270°				
N、PE	0°				
	90°				
	180°				
	270°				

Test Equipment :Surge Tester Model: HCWG

Note: (The Criterion)

A:Normal performance within the specification limits;

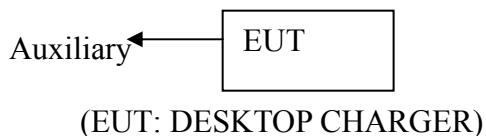
B:Temporary degradation or less of function or performance which is self-recoverable;

C:Temporary degradation or loss of function or performance which requires operator intervention or system reset;

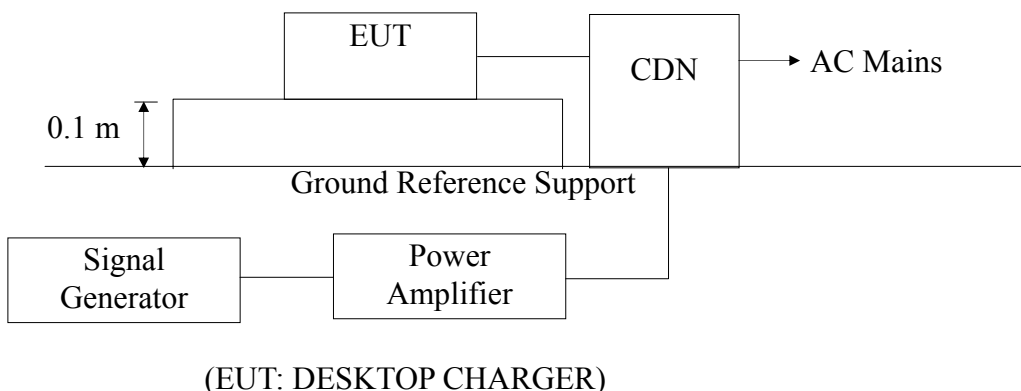
11. INJECTED CURRENTS SUSCEPTIBILITY TEST

11.1 Block Diagram of Test Setup

11.1.1 Block Diagram of the EUT



11.1.2 Block Diagram of Test Setup



11.2 Test Standard

EN 55024:2010+A1:2015 (EN61000-4-6: 2014, Severity Level: Level 2, 3V (rms), (0.15MHz ~ 80MHz)

11.3 Severity Levels and Performance Criterion

11.3.1 Severity level

Level	Field Strength V
1	1
2	3
3	10
X	Special

11.3.2 Performance criterion: A

11.4 Test Procedure

- 1) Set up the EUT, CDN and test generators as shown on Section 11.1.2.
- 2) Let the EUT work in test mode and measure it.
- 3) The EUT are placed on an insulating support 0.1m high above a ground reference plane. CDN (coupling and decoupling device) is placed on the ground plane about 0.3m from EUT. Cables between CDN and EUT are as short as possible, and their height above the ground reference plane shall be between 30 and 50 mm (where possible).
- 4) The disturbance signal described below is injected to EUT through CDN.
- 5) The EUT operates within its operational mode(s) under intended climatic conditions after power on.
- 6) The frequency range is swept from 150KHz to 80MHz using 3V signal level, and with the disturbance signal 80% amplitude modulated with a 1KHz sine wave.
- 7) The rate of sweep shall not exceed 1.5×10^{-3} decades/s. Where the frequency is swept incrementally, the step size shall not exceed 1% of the start and thereafter 1% of the preceding frequency value.
- 8) Recording the EUT operating situation during compliance testing and decide the EUT immunity criterion.

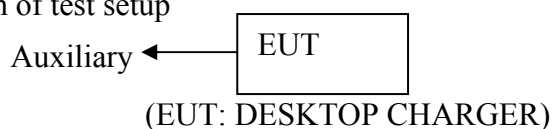
Injected Currents Susceptibility Test Results

EUT	:	DESKTOP CHARGER	Temperature:	20°C
M/N	:	A8101	Humidity	: 50%
Test Voltage	:	AC 230V/50Hz	Test Mode	: Full load
Test Engineer	:	Bill	Test Date	: 2017-11-08
Frequency Range (MHz)		Injected Position	Strength (Unmodulated)	Result
0.15~80		AC Mains	3V	Pass
Frequency Range (MHz)		Injected Position	Strength (Unmodulated)	Result
<p>Test Equipment :Injected Currents Tester Model: CWS500C Injection Clamp Model: F-2031-23MM CDN Model: CDN-M2, CDN-M3</p> <p>Note: (The Criterion) A:Normal performance within the specification limits; B:Temporary degradation or less of function or performance which is self-recoverable; C:Temporary degradation or loss of function or performance which requires operator intervention or system reset;</p>				

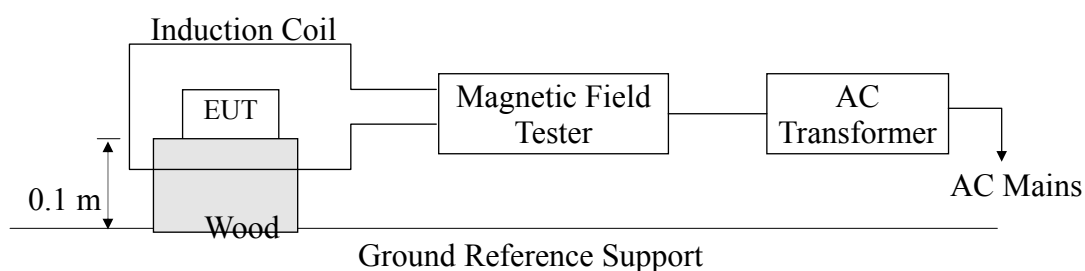
12. MAGNETIC FIELD SUSCEPTIBILITY TEST

12.1 Block Diagram of Test

12.1.1 Block diagram of test setup



12.1.2 Magnetic field test setup



12.2 Test Standard

EN 55024:2010+A1:2015 (EN61000-4-8: 2010, Severity Level: Level 1, 1A / m)

12.3 Severity Levels and Performance Criterion

12.3.1 Severity Levels

Level	Field Strength A/m
1	1
2	3
3	10
4	30
5	100
X	Special

12.3.2 Performance Criterion : A

12.4 Test Procedure

The EUT is placed in the middle of a induction coil (1*1m), under which is a 1*1*0.1m (high) table, this small table is also placed on a larger table,0.8 m above the ground. Both horizontal and vertical polarization of the induction coil are set on test, so that each side of the EUT is affected by the magnetic field. Also can reach the same aim by change the position of the EUT.

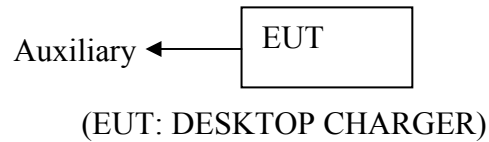
Magnetic Field Immunity Test Result

EUT	:	DESKTOP CHARGER	Temperature:	20°C
M/N	:	A8101	Humidity	: 50%
Test Voltage	:	AC 230V/50Hz	Test Mode	: Full load
Test Engineer	:	Bill	Test Date	: 2017-11-08
Test Level (A/M)		Testing Duration	Coil Orientation	Result
1		5 mins	X	Pass
1		5 mins	Y	Pass
1		5 mins	Z	Pass
Test Level (A/M)		Testing Duration	Coil Orientation	Result
Test Equipment :Magnetic Field Tester Model: HPFM T				
Note: (The Criterion)				
A:Normal performance within the specification limits;				
B:Temporary degradation or less of function or performance which is self-recoverable;				
C:Temporary degradation or loss of function or performance which requires operator intervention or system reset;				

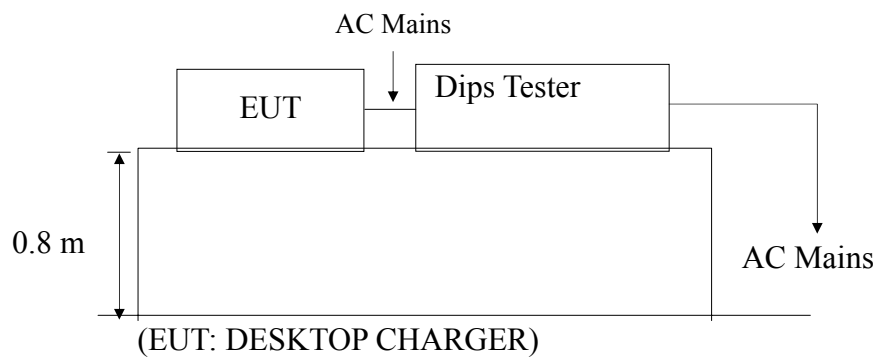
13. VOLTAGE DIPS AND INTERRUPTIONS TEST

13.1 Block Diagram of Test Setup

13.1.1 Block Diagram of the EUT



13.1.2 Dips Test Setup



13.2 Test Standard

EN 55024:2010+A1:2015 (EN61000-4-11: 2004)

13.3 Severity Levels and Performance Criterion

13.3.1 Severity level

Test Level %UT	Voltage dip and short interruptions %UT	Duration (in period)
0	100	0.5 1
40	60	5 10
70	30	25 50 *

13.3.2 Performance criterion : **B&C&C**

13.4 Test Procedure

- 1) Set up the EUT and test generator as shown on Section 13.1.2.
- 2) The interruption is introduced at selected phase angles with specified duration.
- 3) Record any degradation of performance.

Voltage Dips and Interruptions Test Results

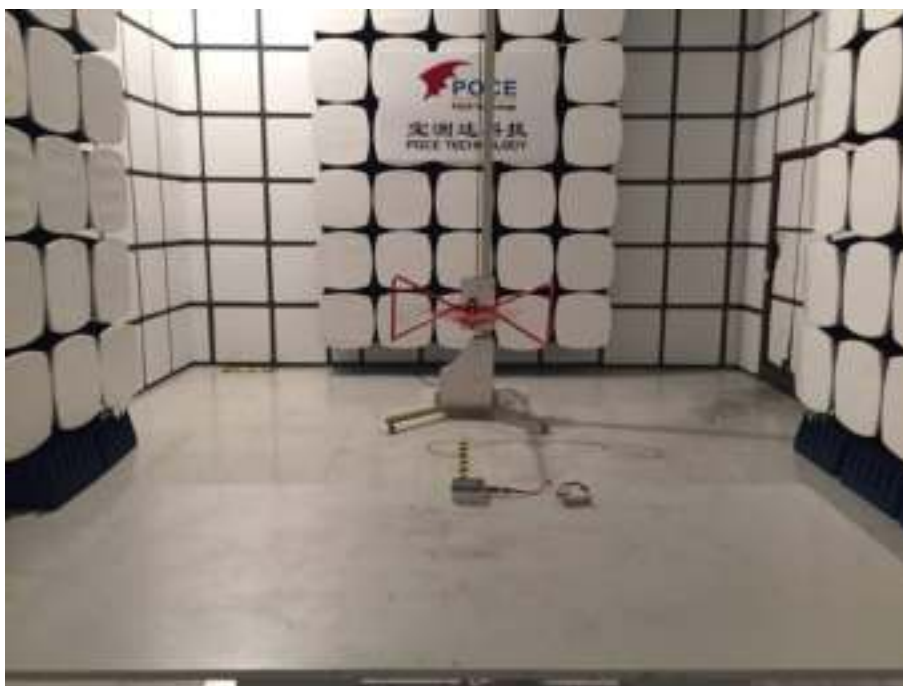
EUT	:	DESKTOP CHARGER	Temperature:	20°C	
M/N	:	A8101	Humidity	: 50%	
Test Voltage	:	AC 230V/50Hz	Test Mode	: Full load	
Test Engineer	:	Bill	Test Date	: 2017-11-08	
Test Level % U _T		Voltage Dips & Short Interruptions % U _T	Duration (in periods)	Criterion	Result
0		100	0.5P	B	Pass
70		30	25P	C	Pass
0		100	250P	C	Pass
Test Level % U _T		Voltage Dips & Short Interruptions % U _T	Duration (in periods)	Criterion	Result
Test Equipment : Voltage Dips And Interruptions Tester Model: HPFS					
<p>Note: (The Criterion)</p> <p>A: Normal performance within the specification limits;</p> <p>B: Temporary degradation or less of function or performance which is self-recoverable;</p> <p>C: Temporary degradation or loss of function or performance which requires operator intervention or system reset;</p>					

14. PHOTOGRAPH

14.1 Photo of Conducted Emission



14.2 Photo of Radiated Emission



APPENDIX

Photo documentation

<p>Photo 1</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input type="checkbox"/> right side</p> <p><input type="checkbox"/> left side</p> <p><input checked="" type="checkbox"/> top</p> <p><input type="checkbox"/> bottom</p> <p><input type="checkbox"/> internal</p>	 <p>A top-down photograph of a white, square-shaped electronic device. The device has four small, oval-shaped indentations at the corners. A ruler is placed to the left and bottom of the device for scale. The ruler shows centimeter markings. The device is centered on a light blue background.</p>
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<p>Photo 2</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input type="checkbox"/> right side</p> <p><input checked="" type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input type="checkbox"/> bottom</p> <p><input type="checkbox"/> internal</p>	 <p>A photograph showing the left side of a white, cylindrical electronic device. The device has four green LED indicators visible on its side. A ruler is placed to the left of the device for scale. The ruler shows centimeter markings. The device is centered on a light blue background.</p>
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Photo documentation

<p>Photo 3</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input type="checkbox"/> right side</p> <p><input type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input checked="" type="checkbox"/> bottom</p> <p><input type="checkbox"/> internal</p>	
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<p>Photo 4</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input checked="" type="checkbox"/> right side</p> <p><input type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input type="checkbox"/> bottom</p> <p><input type="checkbox"/> internal</p>	
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Photo documentation

<p>Photo 5</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input checked="" type="checkbox"/> right side</p> <p><input type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input type="checkbox"/> bottom</p> <p><input type="checkbox"/> internal</p>	
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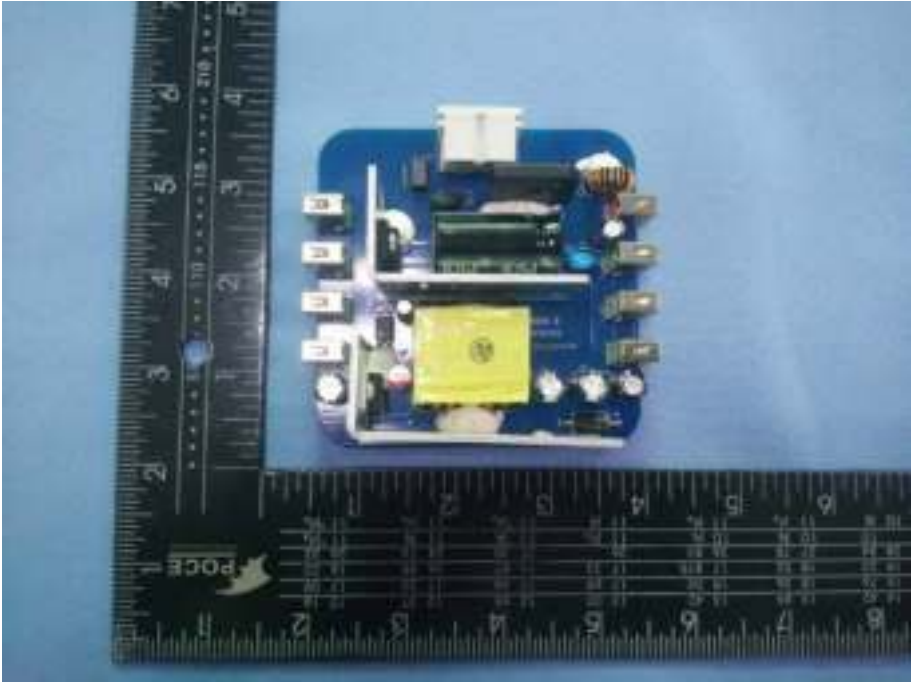
<p>Photo 6</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input type="checkbox"/> right side</p> <p><input checked="" type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input type="checkbox"/> bottom</p> <p><input type="checkbox"/> internal</p>	
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Photo documentation

<p>Photo 7</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input type="checkbox"/> right side</p> <p><input type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input checked="" type="checkbox"/> bottom</p> <p><input type="checkbox"/> internal</p>	 A photograph showing the bottom view of a white, square-shaped component. The component has four small circular holes, one near each corner. It is placed on a blue surface next to a black ruler for scale. The ruler shows markings in centimeters and millimeters.
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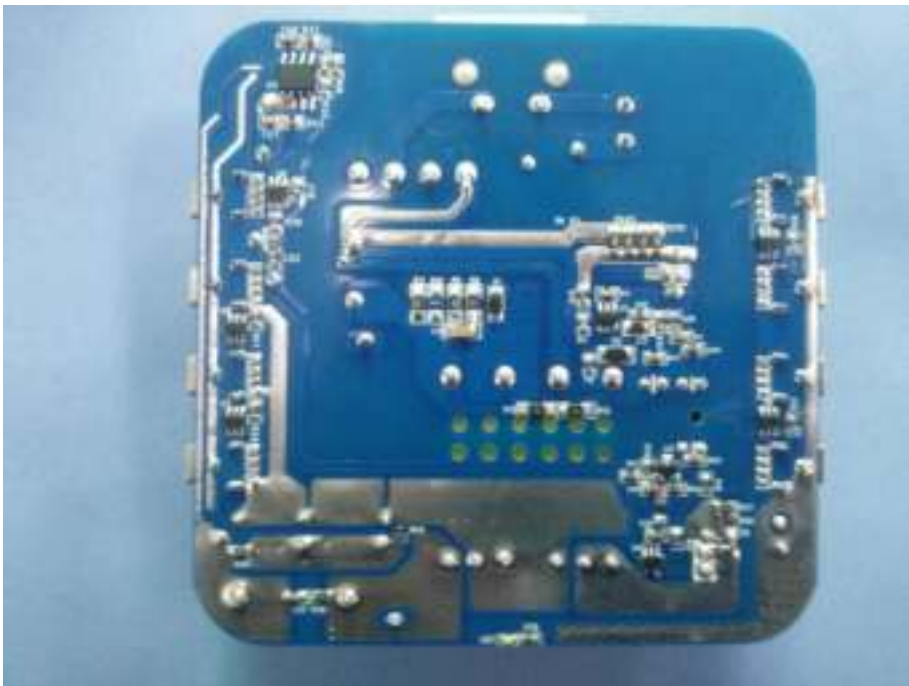
<p>Photo 8</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input checked="" type="checkbox"/> right side</p> <p><input type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input type="checkbox"/> bottom</p> <p><input type="checkbox"/> internal</p>	 A photograph showing the right side view of a white, cylindrical component. The component has a dark grey top and bottom edge. It is placed on a blue surface next to a black ruler for scale. The ruler shows markings in centimeters and millimeters.
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Photo documentation

<p>Photo 9</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input type="checkbox"/> right side</p> <p><input type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input type="checkbox"/> bottom</p> <p><input checked="" type="checkbox"/> internal</p>	 A photograph showing the internal components of a blue printed circuit board (PCB). The board is populated with various electronic components, including a prominent yellow component in the center, several integrated circuits, and surface-mount components. A black ruler is placed vertically to the left and horizontally below the board for scale. The ruler has markings in centimeters and millimeters. The background is a plain, light blue surface.
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<p>Photo 10</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input type="checkbox"/> right side</p> <p><input type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input type="checkbox"/> bottom</p> <p><input checked="" type="checkbox"/> internal</p>	 A photograph showing the internal components of a blue printed circuit board (PCB). The board is populated with various electronic components, including a prominent yellow component in the center, several integrated circuits, and surface-mount components. A black ruler is placed vertically to the left and horizontally below the board for scale. The ruler has markings in centimeters and millimeters. The background is a plain, light blue surface.
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Photo documentation

<p>Photo 11</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input type="checkbox"/> right side</p> <p><input type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input type="checkbox"/> bottom</p> <p><input checked="" type="checkbox"/> internal</p>	
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<p>Photo 12</p> <p>View:</p> <p><input type="checkbox"/> front</p> <p><input type="checkbox"/> rear</p> <p><input type="checkbox"/> right side</p> <p><input type="checkbox"/> left side</p> <p><input type="checkbox"/> top</p> <p><input type="checkbox"/> bottom</p> <p><input checked="" type="checkbox"/> internal</p>	
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*****THE END OF REPORT*****